

Amendments to the Claims:

The following listing of claims will replace all prior versions, and listings, of claims in the application:

1. (Currently Amended) A display device, comprising:

a plurality of write lines;

a plurality of data lines;

a plurality of dots disposed correspondingly to intersections between the plurality of write lines and the plurality of data lines, each of the plurality of dots for displaying, including:

a storing section having memory cells configured by static circuits, each of the static circuits storing a data signal supplied from a respective different one of the plurality of data lines; and

a display control section that performs display control on the basis of the data signal held by the storing section; and

a plurality of read lines disposed correspondingly to the plurality of dots, read out of the data signal held by the storing section being performed when a read signal is transmitted through a respective different one of the plurality of read lines.

2. (Currently Amended) A display device, comprising:

a plurality of write lines;

a plurality of data lines;

a plurality of dots disposed correspondingly to intersections between the plurality of write lines and the plurality of data lines, each of the plurality of dots for displaying, including:

a storing section having a first static circuit and a second static circuit, the first static circuit storing a first data signal supplied through a first data line of the plurality of data

lines, and the second static circuit storing a second data signal supplied through a second data line of the plurality of data lines;

a converting section that converts a value based on a value of the data signal held by the storing section into an analog value; and

a display control section that performs tonal control on the basis of the data signal including the first data signal and the second data signal held by the storing section,

wherein said converting section performing conversion into the analog value at a constant period interval, and a duration that no conversion into the analog value is made being provided in the constant period.

3. (Canceled)

4. (Currently Amended) The display device according to claim 3~~7~~2, the analog value being represented as a PWM waveform generated by the converting section.

5. (Previously Presented) The display device according to claim 2 the analog value including γ -characteristic.

6.-7. (Canceled).

8. (Currently Amended) The display device as claimed in claim 7~~2~~2, each converting section being different in a start time of the constant period, and the period and the duration no conversion into the analog value is made being different.

9. (Currently Amended) The display device as claimed in claim 6~~2~~2, an alternating current drive voltage corresponding to the constant period being applied to said display control section.

10. (Previously Presented) The display device as claimed in claim 9, the alternating current drive voltage being a voltage driven at $V_{COM} \pm V_a$ with respect to a reference voltage V_{COM} .

11. (Previously Presented) The display device as claimed in claim 9, the alternating current drive voltage being a voltage alternating-current-inversion-driven by two voltage-applying lines laid correspondingly to said dot array pattern.

12. (Previously Presented) The display device as claimed in claim 9, a plurality of rows of said dot array being provided by groups, and rows in pair being set in each of the groups to invert a phase of the alternating current drive voltage applied.

13. (Previously Presented) The display device as claimed in claim 2, said display control section controlling light emission of current-driven luminescent devices in connection on the basis of the analog signal in place of performing tonal control using a liquid crystal, thereby effecting tonal control.

14.-18. (Canceled).

19. (Currently Amended) A display device according to claim 1, wherein
——the write signal being supplied to only a dot to be written of the plurality of dots.

20. (Canceled)

21. (Previously Presented) The display device according to claim 1, further comprising a plurality of first lines for supplying a voltage as a power source to the storing section, the plurality of first lines being shared between two rows of the plurality of write signal lines.

22. (Previously Presented) The display device according to claim 1, further comprising:

a column decoder section that selects a data line of the plurality of data lines;

a row decoder sections that selects a write line of the plurality of write lines;

the plurality of dots being included in an active matrix section;

the row decoder section being allocated correspondingly to a length the active-matrix section in a column direction; and

the column decoder section being allocated correspondingly to a length of the active-matrix section in a row direction.

23. (Previously Presented) The display device according to claim 22, further including a column selection switch section that transmits the data signal to a data line of the plurality of data lines selected by the column decoder section.

24. (Previously Presented) The display device according to claim 22, the row decoder that selects a row of the plurality of write lines through which a write signal is transmitted on the basis of an address signal.

25. (Previously Presented) The display device according to claim 22, the column decoder section that selects a data line of the plurality of data lines on the basis of an address signal.

26. (Previously Presented) The display device according to claim 22, further including:

a plurality of pixels each of which being provided by three dots for red, green and blue, respectively, of the plurality of dots;

the column decoder section selecting data lines of the plurality of data lines corresponding to a respective pixel of the plurality of pixels; and

the data signal being supplied together to the three dots included in a respective one of the plurality of pixels.

27. (Previously Presented) The display device according to claim 22, further including:

a plurality of pixels each of which being provided by three dots for red, green and blue, respectively, of the plurality of dots;

the column decoder section selecting data lines of the plurality of data lines corresponding to respective pixels of the plurality of pixels; and

the data signal being supplied together to the three dots included in the respective pixels.

28. (Previously Presented) A display device, comprising:

a substrate;

a plurality of write lines;

a plurality of data lines;

an active-matrix section having a plurality of dots disposed correspondingly to intersections of the plurality of write lines and the plurality of data lines, each of the plurality of dots including an electro-optical conversion section that performs an electro-optical conversion on basis of a data signal supplied through a respective different one of the plurality of data lines;

a column decoder section that selects a data line of the plurality of data lines;

a row decoder section that selects a row of the plurality of write lines through which a write signal is transmitted; and

a timing controller section that controls at least timing of transmitting an address signal on the basis of which at least one of the column decoder section and the decoder section selects at least one data line of the plurality of data lines and the row of the plurality of write lines;

the plurality of write lines, the plurality of data lines, the active-matrix section, the column decoder section, the row decoder section, and the timing controller section being integrally formed on the substrate.

29. (Previously Presented) A display device, comprising:

a substrate;

a plurality of write lines;

a plurality of data lines;

an active-matrix section having a plurality of dots disposed correspondingly to intersections of the plurality of write lines and the plurality of data lines, each of the plurality of dots including an electro-optical conversion section that performs an electro-optical conversion on basis of a data signal supplied through a respective different one of the plurality of data lines;

a column decoder section that selects a data line of the plurality of data lines;

a row decoder section that selects a row of the plurality of write lines through which a write signal is transmitted; and

a memory controller section that controls transmission of the data signals,

the plurality of write lines, the plurality of data lines, the active-matrix section, the column decoder section, the row decoder section, and the memory controller section being integrally formed on the substrate.

30. (Previously Presented) The display device according to claim 23, the column selection switch section being allocated correspondingly to a length of the active-matrix section in a row direction.

31. (Canceled).

32. (Previously Presented) The display device according to claim 1, each of the plurality of dots further comprising an electro-optical conversion section that performs an electro-optical conversion on the basis of a data signal.

33.-35. (Canceled).

36. (Previously Presented) The display device according to claim 1, wherein a number of the memory cells corresponds to a tonal level, a degree of the tonal level being determined by the data signal stored by the memory cells.

37.-38. (Canceled).